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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/879,231
Filing Date June 11, 2001
Inventor Garo J. Derderian, et al.
Assignee Micron Technology, Inc.
Group Art Unit 2818
Examiner T. Lee
Attorney's Docket No. MI22-1748
Title: Capacitor Fabrication Methods and Capacitor Constructions

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98


In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09,653,149, filed August 31, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 17 Oct 2001

By: 
James E. Lake
44,854

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(to be used for all correspondence after initial filing)

Application Number	09/879,231
Filing Date	June 11, 2001
First Named Inventor	Garo J. Derderian
Group Art Unit	2818
Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number MI22-1748

ENCLOSURES (check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	James E. Lake; Reg. No. 44,854; Wells, St. John, et al
Signature	
Date	17 Oct 2001

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